



## 22<sup>nd</sup> IEEE International SOC Conference

September 9-11, 2009 – Queen's University Belfast  
Wellington Park Hotel – Belfast – Northern Ireland – UK

Multibillion transistor System on Chip (SoC) devices, comprised of RF, analog, optical, digital and Micro-Electro-Mechanical Systems (MEMS), are integral parts of ubiquitous communication, entertainment, medical, logistics and carrier products. SoC has been the enabling technology and main thrust behind the evolution of the Internet into a global communication and economic paradigm. Recent advances in systems, deep-submicron and nano process technology are unleashing new opportunities, and also new challenges. These challenges include managing design and verification complexity, EDA tools, on-chip communication, design reuse, handling inevitable faults and the efficient integration of emerging MEMS and nano components into next generation SoCs.

The SOC Conference provides a premier forum for sharing advances in SoC technologies and applications in the areas of digital systems, circuit architectures, design methods, tools and automation, manufacturing and test, and emerging MEMS and nano technologies.

The 22<sup>nd</sup> SOCC will be held in Northern Ireland under the auspices of the Institute of Electronics, Communications and Information Technology (ECIT) and Queen's University Belfast, and will offer three days of technical papers, technical workshops, and a vendor exhibition.

### SUBMISSION OF PAPER AND WORKSHOP/TUTORIAL PROPOSALS

Electronic paper submissions require a full paper in pdf format, limited to four double-column IEEE format pages, and including figures and references. SOCC 2009 also welcomes workshop and tutorial proposals in the related areas of SoC. A proposal with title, a half-page summary, and speaker's short bio should be submitted to the Workshop Chair by **March 31, 2009**. For corporate sponsorship and exhibition, please contact the Exhibition Chair.

The SOCC proceedings will be published by the **IEEE Xplore**<sup>®</sup>. Authors of selected papers with merit will be invited to submit an extended version for possible publication in a special issue of the **IEEE Transactions on Circuits and Systems**.

### SOCC TECHNICAL SCOPE

Papers are invited which address new and previously unpublished results in the area of Systems on Chip, and related topics.

- Analog, Mixed-Signal and RF Circuits and Systems
- Digital Signal Processing (DSP) Circuits and Systems (*Video, Multimedia, Control etc.*)
- Embedded Systems, Multi/Many Core Systems and Embedded Memory Technologies
- Wireline & Wireless Communication Circuits and Systems
- System Level Design Methodology, EDA and Design Tools for SoC
- Reconfigurable and Programmable Circuits and Systems, System on Programmable Devices (*FPGAs*)
- Low-Power Circuits, Systems and Design Methodologies
- Signal Integrity, Design for Testability and Design Verification
- Design for Manufacturability, Variation-aware Methodologies
- Network on Chip (NoC) & Interconnects

### IMPORTANT DATES

Extended Paper submission deadline: **April 27, 2009**  
Final camera-ready paper due: June 26, 2009  
Advance registration: July 31, 2009

Notification of acceptance: June 12, 2009  
Advance registration (authors): July 15, 2009  
Conference date: Sept. 9-11, 2009

### ORGANIZING COMMITTEE

Conference General Chair: *Sakir Sezer, Queen's Univ. Belfast*  
Conference Co-Chair: *Thomas Buechner, IBM*  
Technical Program Chair: *Andrew Marshall, Texas Instruments*

Tech. Program Co-Chair: *Tughrul Arslan, Univ. Edinburgh*  
Steering Committee Chair: *Thanh Tran, Texas Instruments*  
Publicity Chair: *Kajjian Shi, Synopsys*

Please visit the conference web site or contact the SoC Conference office at [info@ieee-socc.org](mailto:info@ieee-socc.org) or +44 28 9097 1703 for registration and current conference information