21st IEEE International SOC Conference

http://www.ieee-socc.org

FINAL CALL FOR PAPERS

September 17-20, 2008

Radisson Hotel Newport Beach California - USA

Multibillion transistor System on Chip (SoC) devices, comprised of RF, analog, optical, digital and Micro-Electro-Mechanical Systems (MEMS), are integral parts of ubiquitous communication, entertainment, medical, logistics and carrier products. SoC has been the enabling technology and main thrust behind the evolution of the Internet into a global communication and economic paradigm. Recent advances in deep-submicron and nano process technology unleash new opportunities but also new challenges at the circuit and system levels. These challenges include managing design and verification complexity, EDA tools, on-chip communication, design reuse, handling inevitable faults and the efficient integration of emerging MEMS and nano components into next generation SoCs.

The SOC Conference provides a premier forum for sharing advances in SoC and nano technologies and applications in the areas of design methods, tools and automation, manufacturing and test, and emerging MEMS and nano technologies. The 21st SOCC will be held in Newport Beach Los Angeles/California and will offer three days of technical papers, a full day of technical workshops, and a vendor exhibition.

On Sep. 16, SOCC will offer a guided tour to NASA JPL in Pasadena.

SUBMISSION OF PAPER AND WORKSHOP/TUTORIAL PROPOSALS

Electronic paper submission requires a full paper in Adobe PDF, limited to four double-column IEEE format pages, including figures and references. SOCC 2008 also solicits workshop and tutorial proposals (half-day or full-day session) in the related areas of SoC. A proposal with title, a half-page summary, and speaker's short bio should be submitted to the Workshop Chair by **April 30, 2008**. Please find more details on the conference web page.

For corporate sponsorship and vendor exhibition, please contact the Exhibition Chair.

SOCC TECHNICAL SCOPE

Papers are invited which address new and previously unpublished results in the area of Systems on Chip, and related areas and topics. SOCC 2008 will feature a special track on Nano and MEMS technologies, devices and design methodologies. Especially industrial contributions related to the state-of-the-art R&D activities in the field of nano, MEMS and SoC design and technology are welcome.

- Analog, Mixed-Signal and RF Circuits and Systems
- Embedded Systems, Multi/Many Core Systems and Embedded Memory Technologies
- Low-Power Circuits, Systems and Design Methodologies
- Signal Integrity, Design for Testability and Design Verification
- Video and Multimedia Processing Circuits and System
- Digital Signal Processing (DSP) Circuits and Systems
- Network on Chip (NoC) & Interconnects
- System Level Design Methodology, EDA and Design Tools for SoC
- Reconfigurable and Programmable Circuits and Systems, System on Programmable Devices (FPGAs)
- Wireline & Wireless Communication Circuits and Systems
- NEMS/MEMS Devices, Nano-technology and Design Methodologies

ORGANIZING COMMITTEE

General Chair: Thanh Tran – Texas Instruments – T.Tran@ieee-socc.org
Technical Program Chair: Thomas Buechner – IBM – T.Buechner@ieee-socc.org
Technical Program Co-Chair: Andrew Marshall – Texas Instruments – A.Marshall@ieee-socc.org

EXTENDED DEADLINE FOR PAPER SUBMISSION: APRIL 30, 2008
NOTIFICATION OF PAPER ACCEPTANCE: JUNE 13, 2008
FINAL CAMERA-READY PAPERS DUE: JULY 18, 2008

Please visit the conference web site or contact the SoC Conference office at info@ieee-socc.org or 301-527-0900 x104 for paper submission, registration and current conference information.

Sponsored by the IEEE Circuits and Systems Society